



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of) Art Unit: 2823
Shunpei YAMAZAKI et al.) Examiner: S. Foong
Serial No. 09/916,484)
Filed: July 30, 2001)
For: ELECTRIC DEVICE, MATRIX)
DEVICE, ELECTRO-OPTICAL DISPLAY))
DEVICE, AND SEMICONDUCTOR)
MEMORY HAVING THIN-FILM)
TRANSISTORS)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 3, 2004.

Clear M. Dickie

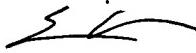
SUBMISSION OF CORRECTED FORMAL DRAWINGS

Commissioner for Patents
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Sir:

In response to the *Notice of Allowability*, enclosed please find 1 sheet of corrected Formal Drawing consisting of Figures 1A-1B and 2A-2B.

Respectfully submitted,


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